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S Eggers

Simultaneous multithreading: a platform for next-generationprocessors - group of 33 »

<u>J Lo</u>

SJ Eggers, JS Emer, HM Leby, JL Lo. RL Stamm, DM ... - Micro, IEEE, 1997 - ieeexplore.ieee.org

D Tulisen

... Multi- threaded superscalars, in addition to heightening instruction ... another thread,

J Emer

thereby exploiting thread-level parallelism. To gauge SMT's potential for ...

H Levy

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Exploiting choice: instruction fetch and issue on an implementable simultaneous multithreading ... - group of 51 » DM Tullsen, SJ Eggers, JS Emer, HM Levy, JL Lo, RL ... - Proceedings of the 23rd annual international symposium on ..., 1996 portal.acm.org

... replicated or resized to support SMT or a multi- threaded workload. ... Our SMT architecture

shares a ... in two ways not possible with single-threaded processors: (1 ...

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Converting Thread-Level Parallelism to Instruction-Level Parallelism via Simultaneous Multithreading - group of 28 » JL LO, SJ EGGERS, JS EMER, HM LEVY. RL STAMM. DM ... - ACM Transactions on Computer Systems. 1997 - portal.acm.org

... The most compelling reason for running parallel applications on an SMT processor

is its ability to use thread-level parallelism and instruction-level ...

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Front-end policies for improved issue efficiency in SMT processors - group of 7 »

A El-Moursy, DH Albonesi - High-Performance Computer Architecture, 2003. HPCA-9 2003. ..., 2003 - ieeexplore.ieee.org

... Figure 5. Statistics for single-threaded and multi-threaded policies for the ... 50%

of the time in single-threaded mode ... usage of queue re-sources in SMT and the ...

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Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor - group of 19 »

DM Tullsen, JL Lo, SJ Eggers, HM Levy - International Symposium on Architectural Support for 2000 - doi.ieeecs.org

... SMT-block • SMT-specRel ... Efficient synchronizatin for multi- threaded processors. ...

Converting thread-level parallelism into Instruction- level parallelism via ...

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An analysis of database workload performance on simultaneous multithreaded processors - group of 19 »

JL Lo, LA Barroso, SJ Eggers, K Gharachorloo, HM ... - ACM SIGARCH Computer Architecture News, 1998 - doi.ieeecomputersociety.org ... a detailed simulation of an SMT processor execut ... DSS workloads on simultaneous multi-

threaded processors. On conventional (single-threaded) processors, a DBMS ...

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Execution-based prediction using speculative slices - group of 18 »

C Zilles, G Sohi - ACM SIGARCH Computer Architecture News, 2001 - doi.ieeecomputersociety.org

... execute as helper threads on a multi- threaded machine. ... the path taken by the main

thread to identify ... context of a simulta- neous multithreading (SMT) processor ...

Cited by 134 - Related Articles - Web Search - BL Direct

Applications of Thread Prioritization in SMT Processors - group of 4 »

SE Raasch, SK Reinhardt - Proc. of the Workshop on Multithreaded Execution And ..., 1999 - eecs.umich.edu

... pronounced in Tullsen et al's study of SMT fetch policies [2 ... If a multi-threaded

processor decides to favor a more resource-efficient thread over a user ...

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Area and system clock effects on SMT/CMP processors - group of 4 »

J Burns, JL Gaudiot - Parallel Architectures and Compilation Techniques, 2001. ..., 2001 - ieeexplore.ieee.org

... Hence single-threaded programs cannot use resources from ... Consequently, we design

the Parallel On-chip Simultaneous ... processors: superscalar, SMT, CMP, and POSM. ...

Cited by 20 - Related Articles - Web Search

Comparing power consumption of an SMT and a CMP DSP for mobile phone workloads - group of 8 »

S Kaxiras, G Narlikar, AD Berenbaum, Z Hu - Proceedings of the international conference on Compilers, ..., 2001 - portal.acm.org

... efficiently, performing as well as a non-multi- threaded DSP but ... For the single-threaded

experi- ments, we run 6 ... In the experiments with the SMT and CMP cores ...

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S Eggers

... and FGMT represents a fine-grained multi- threaded superscalar ... waste, the fine-grained multithread- ed architecture ... than the fine-grained multithreaded processor ...

<u>J Lo</u>

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D Tullsen

H Levy J Emer

Exploiting choice: instruction fetch and issue on an implementable simultaneous multithreading ... - group of 51 » DM Tullsen, SJ Eggers, JS Emer, HM Levy, JL Lo, RL ... - Proceedings of the 23rd annual international symposium on ..., 1996 -

portal.acm.org

... to support SMT or a multi- threaded workload ... of issue, but only fine-grain multithread-

ing of ... several concerns about simultaneous multithreaded processor design ...

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Converting Thread-Level Parallelism to Instruction-Level Parallelism via Simultaneous Multithreading - group of 28 » JL LO, SJ EGGERS, JS EMER, HM LEVY, RL STAMM, DM ... - ACM Transactions on Computer Systems, 1997 - portal.acm.org ... processing on a simultaneous multithread- ing architecture. ... a fine-grained multithreaded

proces- sor ... In contrast, simultaneous multithreading avoids resource ...

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Tuning Compiler Optimizations for Simultaneous Multithreading - group of 31 »

JL Lo. SJ Eggers, HM Levy, SS Parekh, DM Tullsen - International Journal of Parallel Programming, 1999 - Springer ... Fine-grain multithreaded processors context switch as often ... shows simultaneous

multithreading. ... both instruction-level parallelism and thread-level parallelism ...

Cited by 49 - Related Articles - Web Search - BL Direct

<u>Transient fault detection via simultaneous multithreading - group of 19 »</u> SK Reinhardt, SS Mukherjee - ACM SIGARCH Computer Architecture News, 2000 - portal acm.org

. from a Simultaneous Multithreaded (SMT) processor—provides ... Simultaneous multithreading

(SMT) [18][19][21] is a ... 3] implements a four-threaded SMT machine that ...

Cited by 129 - Related Articles - Web Search - BL Direct

AR-SMT: a microarchitectural approach to fault tolerance inmicroprocessors - group of 22 »

E Rotenberg - Fault-Tolerant Computing, 1999. Digest of Papers. Twenty- ..., 1999 - ieeexplore ieee org

... ture, in this case simultaneous multithreading, to achieve ... SMT model exploits

simultaneous multithread- ing to ... work on simulta- neous multithreaded machines [10 ...

Cited by 145 - Related Articles - Web Search - BL Direct

IBM Power5 chip: a dual-core multithreaded processor - group of 17 »

R Kalla, B Sinharoy, JM Tendler - Micro, IEEE, 2004 - ieeexplore.ieee.org

... multithreading (SMT), as in other multithreaded implemen- tations ... Although a higher

level of multithreading is possible ... a group come from the same thread and are ...

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An analysis of database workload performance on simultaneous multithreaded processors - group of 19 »

JL Lo, LA Barroso, SJ Eggers, K Gharachorloo, HM ... - ACM SIGARCH Computer Architecture News, 1998 - doi.ieeecomputersociety.org

... management systems on simultaneous multi- threaded processors. ... 8-wide simultaneous

multithreaded processor ... we demonstrate that simultaneous multithread- ing can ...

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<u>Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor</u> - group of 19 » DM Tullsen, JL Lo, SJ Eggers, HM Levy - International Symposium on Architectural Support for ..., 2000 - doi.ieeecs.org

... Efficient synchronizatin for multi- threaded processors. In Workshop on Multithreaded

Execution Architecture :.. Simultaneous multithread- ing: Maximizing on-chip ...

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Niagara: a 32-way multithreaded Sparc processor - group of 24 »

P Kongetira, K Aingaran, K Olukotun - IEEE Micro, 2005 - doi.ieeecomputersociety.org ... called time-sliced or vertical multithreaded. ... multithreading (SMT) or, alternately,

horizontal multithreading. ... instantiating single-threaded processor cores on ...

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A Clustered Approach to Multithreaded Processors - group of 9 »

J Lo

V Krishnan, J Torrellas - Proceedings of the International Parallel Processing ..., 1998 - doi.ieeecs.org ... SMT architec- ture when running parallel applications. ... the SMT4 processor has 4 clusters.

S Eggers

each supporting 2 ... is much smaller than that of the centralized SMT. ...

H Levy

Cited by 15 - Related Articles - Web Search

D Tullsen
D Brooks

Area and system clock effects on SMT/CMP processors - group of 4 »

J Burns, JL Gaudiot - Parallel Architectures and Compilation Techniques, 2001. ..., 2001 - ieeexplore.ieee.org

... Consequently, we design the Parallel On-chip Simultaneous Multithreaded ... create two

partitions within the SMT.pl .f2 ... extra stage delay of inter cluster forwarding ...

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The impact of resource partitioning on SMT processors - group of 12 »

SE Raasch, SK Reinhardt - Parallel Architectures and Compilation Techniques, 2003. ... - ieeexplore.ieee.org

... insights to the design of SMT proces- sors ... Allowing threads to share clusters—and

thus execution ... 12th International Conference on Parallel Architectures and ...

Cited by 29 - Related Articles - Web Search

Tuning Compiler Optimizations for Simultaneous Multithreading - group of 31 »

JL Lo, SJ Eggers, HM Levy, SS Parekh, DM Tullsen - International Journal of Parallel Programming, 1999 - Springer

... A true SMT workload would be multiprogrammed: for example ... 8 threads for the first

parallel application, then ... use a cyclic distribution to cluster the accesses ...

Cited by 49 - Related Articles - Web Search - BL Direct

<u>Power-aware microarchitecture: design and modeling challenges fornext-generation microprocessors</u> - group of 16 »

DM Brooks, P Bose, SE Schuster, H Jacobson, PN ... - Micro, IEEE, 2000 - ieeexplore.ieee.org

... analysis at the cir- cuit and microarchitecture levels) of an adaptive issue queue ...

monitored and fed back to control the progress of later instruction processing ...

Cited by 133 - Related Articles - Web Search - BL Direct

Instruction-level distributed processing - group of 13 »

JE Smith - Computer, 2001 - Springer

... are used, especially if multiple parallel copies of ... the processor is shared as in

SMT, but any ... 21264 microarchitecture there are two clusters, with different ...

Cited by 23 - Related Articles - Web Search - BL Direct

Exploiting fine-grain thread level parallelism on the MIT multi-ALU processor - group of 12 »

SW Keckler, WJ Dally, D Maskit, NP Carter, A Chang ... - Proceedings of the 25th annual international symposium on

..., 1998 - portal.acm.org

... A synchronization pipeline stage holds instructions from each ... to consecutive addresses

to proceed in parallel. ... Each cluster has its own 4KB instruction cache ...

Cited by 41 - Related Articles - Web Search - BL Direct

An Empirical Study of Hyper-Threading in High Performance Computing Clusters - group of 5 »

T Leng, R Ali, J Hsieh, V Mashayekhi, R ... - Linux HPC Revolution, 2002 - nubes.uspnet.usp.br

... Intel's Hyper-Threading implements SMT in such a way that each ... regardless of the

number of nodes or CPUs of the cluster. ... NAS Parallel Benchmark - FT (Class B ...

Cited by 7 - Related Articles - View as HTML - Web Search

Symbiotic jobscheduling with priorities for a simultaneous multithreading processor - group of 13 »

A Snavely, DM Tullsen, G Voelker - ACM SIGMETRICS Performance Evaluation Review, 2002 - portal.acm.org

... level parallelism in the instruction issue queues. The multiple threads which will

run on an SMT processor might come from parallel applications, multiple ...

Cited by 41 - Related Articles - Web Search - BL Direct

... netwrk-on-chip architecture for digital signal processing based on simultaneous multithreading (SMT ...

M Berekovic, P Pirsch - 5th Workshop on Media and Streaming Processor - pdcl.eng.wayne.edu

... 32], significantly improves performance on parallel workloads without ... decoupled

(Figure 2). SMT processors with ... for communication between the clusters and thus ...

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